



501.40201X00

AF

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

Applicants: TADOKORO, et al.

Serial No.: 09/893,577

Filed: June 29, 2001

For: Fabrication Method Of Semiconductor Integrated
Circuit Device

Art Unit: 1765

Examiner: Kin Chen

RECEIVED

DEC 15 2003

TC 1700

**NOTICE OF APPEAL FROM THE PRIMARY
EXAMINER TO THE BOARD OF APPEALS**

Mail Stop AF
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 10, 2003

Sir:

In accordance with the provisions of 37 CFR 1.191, Applicants hereby appeal from the final rejection as set forth in the Official Action mailed June 10, 2003, finally rejecting claims 1-6 and 8-40.

A Credit Card Payment form in the amount of \$330.00 is enclosed, to cover the Appeal Fee.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

William I. Solomon
Registration No. 28,565

1300 North Seventeenth Street
Suite 1800
Arlington, VA 22209
Tel.: 703-312-6600
Fax.: 703-312-6666
WIS/sjg